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Inventor Information for 10/022659

Inventor Name	City	State/Country
AZADET, KAMERAN	MORGANVILLE	NEW JERSEY
HARATSCH, ERICH FRANZ	BRADLEY BEACH	NEW JERSEY

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Inventor Name Search Result

Your Search was:

Last Name = AZADET
First Name = KAMERAN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08426510	5625304	150	04/21/1995	VOLTAGE COMPARATOR REQUIRING NO COMPENSATING OFFSET VOLTAGE	AZADET, KAMERAN
08713687	5983254	150	09/17/1996	ZERO-LATENCY PIPELINE ARCHITECTURE FOR DIGITAL FILTERS	AZADET, KAMERAN
09188625	6584159	150	11/09/1998	MIXED-MODE NEXT/ECHO CANCELLER FOR PULSE AMPLITUDE MODULATED (PAM) SIGNALS	AZADET, KAMERAN
09203969	Not Issued	161	12/02/1998	METHOD AND APPARATUS FOR REDUCING DISPERSION PROBLEMS IN FIBER CHANNELS	AZADET, KAMERAN
09206519	6202075	150	12/07/1998	SECOND ORDER LMS TAP UPDATE ALGORITHM WITH HIGH TRACKING CAPABILITY	AZADET, KAMERAN
09206527	6363112	150	12/07/1998	PARALLEL PROCESSING DECISION FEEDBACK EQUALIZER	AZADET, KAMERAN
09326781	6192072	150	06/04/1999	PARALLEL PROCESSING DECISION-FEEDBACK EQUALIZER (DFE) WITH LOOK-AHEAD PROCESSING	AZADET, KAMERAN
09414129	6617993	150	10/08/1999	ANALOG TO DIGITAL COVERTER USING ASYNCHRONOUSLY SWEPT THERMOMETER CODES	AZADET, KAMERAN
09471920	6999521	150	12/23/1999	METHOD AND APPARATUS FOR SHORTENING THE CRITICAL PATH OF REDUCED COMPLEXITY SEQUENCE	AZADET, KAMERAN

				ESTIMATION TECHNIQUES	
09834668	7000175	150	04/13/2001	METHOD AND APPARATUS FOR PIPELINED JOINT EQUALIZATION AND DECODING FOR GIGABIT COMMUNICATIONS	AZADET, KAMERAN
10022659	Not Issued	120	12/18/2001	Method and apparatus for joint equalization and decoding of multilevel codes	AZADET, KAMERAN
10022665	Not Issued	133	12/18/2001	Method and apparatus for joint equalization and decoding of multidimensional codes transmitted over multiple symbol durations	AZADET, KAMERAN
10106990	6771198	150	03/25/2002	METHOD AND APPARATUS FOR CONVERTING BETWEEN ANALOG AND DIGITAL DOMAINS USING FREQUENCY INTERLEAVING	AZADET, KAMERAN
10122997	7085794	150	04/12/2002	LOW POWER VECTOR SUMMATION METHOD AND APPARATUS	AZADET, KAMERAN
10219904	7155134	150	08/15/2002	PULSE AMPLITUDE MODULATED TRANSMISSION SCHEME FOR OPTICAL CHANNELS WITH SOFT DECISION DECODING	AZADET, KAMERAN
10219905	Not Issued	71	08/15/2002	Optically calibrated pulse amplitude modulated transmission scheme for optical channels	AZADET, KAMERAN
10219906	Not Issued	71	08/15/2002	Method and apparatus for cross-talk cancellation in frequency division multiplexed transmission systems	AZADET, KAMERAN
10229623	Not Issued	41	08/28/2002	Single sideband dense wavelength division multiplexed optical transmission scheme	AZADET, KAMERAN
10348871	7164711	150	01/22/2003	PROGRAMMABLE RECEIVE-SIDE CHANNEL EQUALIZER	AZADET, KAMERAN
10378096	7257329	150	02/28/2003	DUOBINARY PULSE SHAPING FOR OPTICAL TRANSMISSION SYSTEMS EMPLOYING PULSE AMPLITUDE MODULATION TECHNIQUES	AZADET, KAMERAN
10610334	Not	41	06/30/2003	Method and apparatus for reducing	AZADET,

	Issued			cross-talk with reduced redundancies	KAMERAN
10610335	Not Issued	41	06/30/2003	Method and apparatus for reducing noise in an unbalanced channel using common mode component	AZADET, KAMERAN
10610336	7263541	150	06/30/2003	MULTI-DIMENSIONAL HYBRID AND TRANSPOSE FORM FINITE IMPULSE RESPONSE FILTERS	AZADET, KAMERAN
10874834	Not Issued	41	06/23/2004	Method and apparatus for power management using transmission mode with reduced power	AZADET, KAMERAN
10880331	Not Issued	71	06/29/2004	Universal controller for peripheral devices in a computing system	AZADET, KAMERAN
10924280	Not Issued	123	08/23/2004	Peripheral controller with shared EEPROM	AZADET, KAMERAN
10945663	Not Issued	30	09/21/2004	Methods and apparatus for interface adapter integrated virus protection	AZADET, KAMERAN
11141337	Not Issued	41	05/31/2005	Common-mode shifting circuit for CML buffers	AZADET, KAMERAN
11191895	Not Issued	30	07/28/2005	Digital phase-locked loop	AZADET, KAMERAN
11234446	Not Issued	61	09/26/2005	Method and apparatus for pipelined joint equalization and decoding for gigabit communications	AZADET, KAMERAN
11256182	Not Issued	30	10/21/2005	Hybrid memory architecture for reduced state sequence estimation (RSSE) techniques	AZADET, KAMERAN
11359201	Not Issued	71	02/22/2006	Low power vector summation method and apparatus	AZADET, KAMERAN
11561296	Not Issued	30	11/17/2006	METHODS AND APPARATUS FOR SOFT DECISION DECODING USING RELIABILITY VALUES BASED ON A LOG BASE TWO FUNCTION	AZADET, KAMERAN
11781313	Not Issued	17	07/23/2007	Multi-Dimensional Hybrid And Transpose Form Finite Impulse Response Filters	AZADET, KAMERAN
60180079	Not Issued	159	02/03/2000	Zero-overhead coding for LAN serial and WDM solutions	AZADET, KAMERAN
60245519	Not Issued	159	11/03/2000	Pipelined joint equalization and decoding for gigabit	AZADET, KAMERAN

				communications	
60347462	Not Issued	159	01/11/2002	Frequency interleaved analog to digital conversion	AZADET, KAMERAN
60366923	Not Issued	159	03/22/2002	Bandwidth efficient multicarrier transmission system with cross-talk cancellation	AZADET, KAMERAN
60366930	Not Issued	159	03/22/2002	Opto-electronically calibrated PAM transceiver	AZADET, KAMERAN
60387828	Not Issued	159	06/10/2002	Dense wavelength division multiplexing (DWDM) single sideband (SSB) optical transmission scheme based on optical MUX/DEMUX	AZADET, KAMERAN
60412870	Not Issued	159	09/23/2002	Duobinary pulse shaping for PAM transmission over optical channels	AZADET, KAMERAN
60443258	Not Issued	159	01/28/2003	Multi-dimensional hybrid-form FIR filter architecture	AZADET, KAMERAN
60443260	Not Issued	159	01/28/2003	Zero-redundancy cross-talk cancellation architecture	AZADET, KAMERAN
60443261	Not Issued	159	01/28/2003	Cross-talk canceller and equalizer architecture for unbalanced channels	AZADET, KAMERAN
60525229	Not Issued	159	11/25/2003	Universal controller for PC peripherals	AZADET, KAMERAN
60525230	Not Issued	159	11/25/2003	Low pin-count controller with shared EEPROM	AZADET, KAMERAN
60525231	Not Issued	159	11/25/2003	Software-directed power management for Ethernet network device	AZADET, KAMERAN

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Your Search was:

Last Name = HARATSCH
First Name = ERICH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09031728	6154222	150	02/27/1998	METHOD FOR DEFINING ANIMATION PARAMETERS FOR AN ANIMATION DEFINITION INTERFACE	HARATSCH, ERICH
09113747	Not Issued	71	07/10/1998	INTERACTION MODALITIES FOR MULTIMEDIA DELIVERY AND PRESENTATION	HARATSCH, ERICH
09113748	6317131	150	07/10/1998	INTERACTION MODALITIES FOR MULTIMEDIA DELIVERY AND PRESENTATION USING NODES	HARATSCH, ERICH
09686077	6483513	150	10/12/2000	Method for defining MPEP 4 animation parameters for an animation definition interface	HARATSCH, ERICH
09950792	Not Issued	161	09/13/2001	Interaction modalities for multimedia delivery and presentation using nodes	HARATSCH, ERICH
09975052	Not Issued	161	10/12/2001	Method for defining animation parameters for an animation definition interface	HARATSCH, ERICH
10291428	6970172	150	11/12/2002	SYSTEM FOR DEFINING MPEG 4 ANIMATION PARAMETERS FOR AN ANIMATION DEFINITION INTERFACE	HARATSCH, ERICH
11179715	Not Issued	93	07/12/2005	METHOD FOR DEFINING ANIMATION PARAMETERS FOR AN ANIMATION DEFINITION INTERFACE	HARATSCH, ERICH
60041732	Not Issued	159	03/27/1997	ANIMATION DEFINITION INTERFACE	HARATSCH, ERICH

60042799	Not Issued	159	04/07/1997	LET ANIMALS AND FURNITURE SPEAK: PROPOSAL FOR EXTENDING THE SCOPE OF FACE AND BODY ANIMATION	HARATSCH, ERICH
60052576	Not Issued	159	07/15/1997	ENSURING QUALITY OF SERVICE FOR SCENE GRAPHS WITH BIFS	HARATSCH, ERICH
11496121	Not Issued	25	07/31/2006	Methods and apparatus for low-density parity check decoding using hardware-sharing and serial sum-product architecture	HARATSCH, ERICH F.
11535540	Not Issued	25	09/27/2006	Systems and Methods for Generating Erasure Flags	HARATSCH, ERICH F.
11620988	Not Issued	25	01/08/2007	SYSTEMS AND METHODS FOR PRIORITIZING ERROR CORRECTION DATA	HARATSCH, ERICH F.
09834668	7000175	150	04/13/2001	METHOD AND APPARATUS FOR PIPELINED JOINT EQUALIZATION AND DECODING FOR GIGABIT COMMUNICATIONS	HARATSCH, ERICH FRANZ
10022659	Not Issued	120	12/18/2001	Method and apparatus for joint equalization and decoding of multilevel codes	HARATSCH, ERICH FRANZ
10022665	Not Issued	133	12/18/2001	Method and apparatus for joint equalization and decoding of multidimensional codes transmitted over multiple symbol durations	HARATSCH, ERICH FRANZ
10853087	Not Issued	71	05/25/2004	Method and apparatus for multiple step viterbi detection with local feedback	HARATSCH, ERICH FRANZ
10853088	Not Issued	41	05/25/2004	Method and apparatus for precomputation and pipelined selection of intersymbol interference estimates in a reduced-state Viterbi detector	HARATSCH, ERICH FRANZ
10853089	Not Issued	71	05/25/2004	Method and apparatus for precomputation and pipelined selection of branch metrics in a reduced-state Viterbi detector	HARATSCH, ERICH FRANZ
10853090	Not Issued	161	05/25/2004	Method and apparatus for reduced-state Viterbi detection in a read channel of a magnetic recording system	HARATSCH, ERICH FRANZ

10962188	Not Issued	71	10/08/2004	Pipelined decision-feedback unit in a reduced-state viterbi detector with local feedback	HARATSCH, ERICH FRANZ
11045585	Not Issued	30	01/28/2005	Method and apparatus for soft-output viterbi detection using a multiple-step trellis	HARATSCH, ERICH FRANZ
11119038	Not Issued	30	04/29/2005	Method and apparatus for iterative error-erasure decoding	HARATSCH, ERICH FRANZ
11234446	Not Issued	61	09/26/2005	Method and apparatus for pipelined joint equalization and decoding for gigabit communications	HARATSCH, ERICH FRANZ
11256182	Not Issued	30	10/21/2005	Hybrid memory architecture for reduced state sequence estimation (RSSE) techniques	HARATSCH, ERICH FRANZ
11691847	Not Issued	25	03/27/2007	METHOD AND APPARATUS FOR REDUCED-STATE VITERBI DETECTION IN A READ CHANNEL OF A MAGNETIC RECORDING SYSTEM	HARATSCH, ERICH FRANZ
60244453	Not Issued	159	10/30/2000	Low complexity joint equalizer and decoder for 1000base-t gigabit ethernet	HARATSCH, ERICH FRANZ
60245519	Not Issued	159	11/03/2000	Pipelined joint equalization and decoding for gigabit communications	HARATSCH, ERICH FRANZ
09326785	6690754	150	06/04/1999	METHOD AND APPARATUS FOR REDUCING THE COMPUTATIONAL COMPLEXITY AND RELAXING THE CRITICAL PATH OF REDUCED STATE SEQUENCE ESTIMATION (RSSE) TECHNIQUES	HARATSCH, ERICH FRANZ
09471920	6999521	150	12/23/1999	METHOD AND APPARATUS FOR SHORTENING THE CRITICAL PATH OF REDUCED COMPLEXITY SEQUENCE ESTIMATION TECHNIQUES	HARATSCH, ERICH FRANZ

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